

### AMENDMENTS TO THE SPECIFICATION

Please replace the last paragraph on page 5 (going to the top of page 6) with the following amended paragraph:

The outputs of the master and slave latches 16 and 18 provide inputs to first and second differential pairs Q1/Q2 and Q3/Q4 of the switch 20 of the present invention. In the illustrative embodiment, NPN (in reference to N-type semiconductor material and P-P-type semiconductor material associated with NPN and PNP junction transistors) technology is employed. However, those skilled in the art will appreciate that PNP for FET technology may be employed as without departing from the scope of the present teachings.